

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of

HASEGAWA et al.

Application Number: 10/058,787

Filed: January 30, 2002

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,  
METHOD OF TESTING SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE AND METHOD OF  
MANUFACTURING SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

ATTORNEY DOCKET NO. ASAM.0038

Unit 2813

Examiner  
Dolan, Jennifer M

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**COVER LETTER**

Sir:

[ x ] The fee for submission of claims is calculated as shown below:

FOR	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS PAID	RATE	CALCULATION
Total Claims	10	12	(Over 20)	x \$18	0
Independent Claims	3	3	(Over 3)	x \$86	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$290	0
REDUCTION FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1.27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED				x ½	
				TOTAL	0

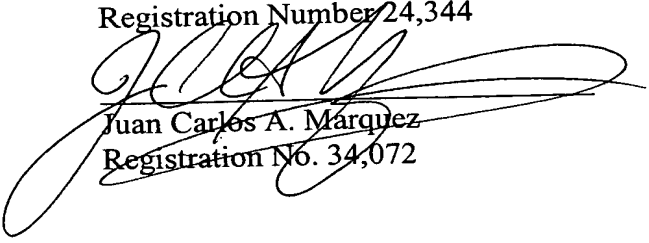
In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

- |  |   |
|--|---|
| <input checked="" type="checkbox"/> Response to Office Action<br>(with Claim Amendments) | <input type="checkbox"/> Petition for Extension of Time ( month)      |
| <input type="checkbox"/> Substitute Specification & marked-up copy                       | <input type="checkbox"/> Terminal Disclaimer                          |
| <input type="checkbox"/> Preliminary Amendment   | <input type="checkbox"/> Letter to Draftsperson w/ sheets of drawings |
| <input type="checkbox"/> Other _____   | <input type="checkbox"/> Assignment                                   |
|  | <input type="checkbox"/> Petition under _____                         |

- [ ] Please charge my **Deposit Account Number** \_\_\_\_\_ in the amount of \_\_\_\_\_ to cover the fees for \_\_\_\_\_. A duplicate copy of this paper is enclosed.
- [ ] A check in the amount of \$ \_\_\_\_\_ to cover the fee is enclosed.
- [x] The Commissioner is hereby authorized to charge any additional fees associated with this communication, or credit any overpayment to **Deposit Account Number 08-1480**.

Respectfully submitted,

\_\_\_\_\_  
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**March 15, 2004**



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re U.S. Patent Application of</b>	)	
	)	
<b>HASEGAWA et al.</b>	)	
	)	<b>Art Unit 2813</b>
<b>Application Number: 10/058,787</b>	)	
	)	<b>Examiner</b>
<b>Filed: January 30, 2002</b>	)	<b>Dolan, Jennifer M</b>
	)	
<b>For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,</b>	)	
<b>METHOD OF TESTING SEMICONDUCTOR</b>	)	
<b>INTEGRATED CIRCUIT DEVICE AND METHOD OF</b>	)	
<b>MANUFACTURING SEMICONDUCTOR INTEGRATED</b>	)	
<b>CIRCUIT DEVICE</b>	)	
	)	
<b>Attorney Docket No. ASAM.0038</b>	)	
	)	
<b>Honorable Assistant Commissioner</b>		
<b>for Patents</b>		
<b>Washington, D.C. 20231</b>		

**RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

This is in response to the Office Action dated December 17, 2003, the period for response to which expires on March 17, 2004. Please amend the above-identified application as follows: